

ABSTRACT

Multi-layer memory arrays and methods are provided. A memory array has two or more layers of memory material, each layer of memory material having an array of memory cells. A first contact penetrates through each layer of memory material in a first plane and is electrically connected to each layer of memory material so as to electrically interconnect the layers of memory material in the first plane. A second contact penetrates through at least one of the layers of memory material in a second plane substantially perpendicular to the first plane.